



# MCP7940N

## MCP7940N Family Silicon Errata

The MCP7940N family devices that you have received conform functionally to the current Device Data Sheet (DS20005010F), except for the anomalies described in this document.

The silicon issues discussed in the following pages are for devices listed in [Table 1](#). The silicon issues are summarized in [Table 2](#).

The errata described in this document will be addressed in future revisions of the MCP7940N silicon.

**TABLE 1: AFFECTED PART NUMBERS**

Part Number
MCP7940N

**Note:** This document summarizes all silicon errata issues from all revisions of silicon, previous as well as current. Only the issues indicated in the last column of [Table 2](#) apply to the current silicon revision.

**Note:** For more information on identifying the product date code, refer to Packaging Information section of the product Data Sheet or contact your local Microchip sales office.

**TABLE 2: SILICON ISSUE SUMMARY**

Issue Number	Issue Summary	Affected Date Codes <sup>(1, 2)</sup>
		All
1	Date incrementing at noon.	X
2	Spurious alarm interrupts when matching on minutes.	X

**Note 1:** Only those issues indicated in the last column apply to the current silicon revision.

**Note 2:** The date codes are presented in YYWW format.

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## Silicon Errata Issues

**Note:** This document summarizes all silicon errata issues from all revisions of silicon, previous as well as current. Only the issues indicated by the shaded column in the following tables apply to the current silicon revision.

### 1. Issue: Date Increment

When operating in 12-hour mode (RTCHOUR<6> is set) if the application loads an hour value before 12:00 PM while the oscillator is running then the date and day of week may increment at 12:00 PM. When this occurs, the month and year will also increment according to the normal rollover rules. The date will increment again at 12:00 AM.

#### Work around

Disable the oscillator by ensuring both the ST and EXTOSC bits are cleared and wait for the OSCRUN bit to clear before loading the new hour value.

#### Affected Silicon Revisions

All
X

### 2. Issue: Spurious Minute-Match Alarm Interrupt

When using an alarm to match on minutes (ALMxMSK<2:0> = 001) and digital trimming is being used to slow down the time (TRIMVAL<6:0> > 0 and SIGN = 0), spurious alarm interrupts may occur at incorrect minutes.

#### Work around

When an alarm interrupt occurs, read the RTCMIN register and confirm the minute matches the desired value for the alarm.

#### Affected Silicon Revisions

All
X

## APPENDIX A: DOCUMENT REVISION HISTORY

### Rev A Document (04/2014)

Initial release of this document.

### Rev B Document (12/2015)

Added Silicon Issue 2 (spurious alarm interrupts when matching on minutes).

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NOTES:

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